

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously Presented) A display apparatus comprising:  
a plurality of thin film transistors, each of said thin film transistor comprising a semiconductor thin film constituting a channel and having a threshold voltage, and a first gate electrode on one side of said semiconductor thin film and a second gate electrode on an opposite side of said semiconductor thin film,

and further comprising a means for adjusting the threshold voltage by applying a first threshold adjustment voltage to the second gate electrode when the first gate electrode receives a first control voltage and applying a second threshold adjustment voltage different than the first threshold adjustment voltage to the second gate electrode when the first electrode receives a second control voltage.

2. (Previously Presented) The semiconductor apparatus according to claim 1, wherein said semiconductor thin film constituting said channel is comprised of polycrystalline silicon which does not contain an impurity affecting the formation of a depletion layer, and has a thickness of 100 nm or less.

3. (Previously Presented) The semiconductor apparatus according to claim 1, wherein said semiconductor thin film constituting said channel is comprised of polycrystalline silicon which contains an impurity effectively affecting the formation of a depletion layer, and has a thickness two times or less the maximum of the thickness of said depletion layer.

Claims 4-38 (Canceled).

39. (Previously Presented) A display apparatus comprising:

a plurality of thin film transistors, each of said thin film transistor comprising a semiconductor thin film constituting a channel and having a threshold voltage, and a first gate electrode above said semiconductor thin film and a second gate electrode below said semiconductor thin film,

and further comprising a means for adjusting the threshold voltage by applying a first threshold adjustment voltage to the second gate electrode when the first gate electrode receives a first control voltage and applying a second threshold adjustment voltage, different than the first threshold adjustment voltage, to the second gate electrode when the first electrode receives a second control voltage.

40. (Previously Presented) A display apparatus according to claim 1, further wherein the voltage applied to the first gate electrode is different from the threshold adjustment voltage applied to the second gate electrode during voltage application.

41. (Previously Presented) A display apparatus according to claim 39, further wherein the voltage applied to the first gate electrode is different from the threshold adjustment voltage applied to the second gate electrode during voltage application.